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S14	889277	CCW chain	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/14 16:15
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S41	124	(process$3 near5 command$1) near10 (received near3 order)	USPAT	OR	OFF	2005/04/25 16:16
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S45	0	("06and21").PN.	USPAT; USOCR	OR	OFF	2005/04/25 16:06
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S64	970	(711/100).CCLS.	USPAT; USOCR	OR	OFF	2005/04/26 09:56
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S66	1021	(711/114).CCLS.	USPAT; USOCR	OR	OFF	2005/04/26 09:57
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S68	481	(711/169).CCLS.	USPAT; USOCR	OR	OFF	2005/04/26 09:58
S69	3194	CCW or (channel adj command adj word)	USPAT	OR	OFF	2005/04/26 09:59
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1 **Architecture of the IBM system/370**   ■
Richard P. Case, Andris Padegs
January 1978 **Communications of the ACM**, Volume 21 Issue 1

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This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

2 **The local disk controller**   ■
Gilbert E. Houtekamer
August 1985 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1985 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume 13 Issue 2

Full text available: 🗎 pdf(1.02 MB)   Additional Information: full citation, abstract, references, citings, index terms

The performance of the I/O subsystem in the 370-XA architecture has been improved considerably with the introduction of the new channel subsystem, as compared to the System/370 architecture. The emphasis in the 370-XA architecture is on reducing the CPU load associated with I/O, and on reducing the congestion in multi-CPU, shared systems, by redesigning the channel system. In this paper we will show that a reallocation of the control unit logic may triple the channel subsystem's ...

3 **The architecture of the SPERRY UNIVAC 1100 series systems**   ■
B. R. Borgerson, M. D. Godfrey, P. E. Hagerty, T. R. Rykken
April 1979 **Proceedings of the 6th annual symposium on Computer architecture**

Full text available: Additional Information: full citation, abstract, references, citings, index

📄 pdf(841.19 KB)   <u>terms</u>

This paper presents an overview of the architecture of the SPERRY UNIVAC® 1100 Series systems. The principal topics are instruction and data formats, main storage and addressing, process management, and I/O.

4 Prototype II: A job selection simulation model

Thomas A. Byrne, Alan V. Piercey, Frank L. Myers
June 1973 **Proceedings of the 1st symposium on Simulation of computer systems**

Full text available: 📄 pdf(995.35 KB)   Additional Information: <u>full citation, abstract, index terms</u>

Optimization of a multiprogramming computer configuration is a complex task. The most common approach in establishing a mix of resources and a classing/initiator configuration is to make an educated guess and, over a period of time, to make refinements through a series of increasingly more educated guesses. This paper describes an alternate approach to optimization of resources and job selection through simulation. This use of simulation departs from the accepted simulation philosophy in th ...

5 Evolutionary computer architecture: the unidata 7.000 series

Helmut Berndt
April 1976 **ACM SIGARCH Computer Architecture News**, Volume 5 Issue 1

Full text available: 📄 pdf(735.56 KB)   Additional Information: <u>full citation, references</u>

6 ASSIST-V: A tool for studying the implementation of operating systems

Charles E. Hughes, Charles P. Pfleeger
February 1976 **Proceedings of the ACM SIGCSE-SIGCUE technical symposium on Computer science and education**, Volume 2 , 8 Issue SI , 1

Full text available: 📄 pdf(619.30 KB)   Additional Information: <u>full citation, abstract, references, citings, index terms</u>

This paper describes a program called ASSIST-V which is designed to provide students and other researchers with an environment in which they may write and analyze operating systems for the IBM 360/370 series of machines. ASSIST-V is an extended version of a student-oriented assembler/interpreter called ASSIST. A user of ASSIST-V is presented with a machine which simulates the full set of S/360 machine instructions (including privileged instructions): the standard 360/370 interrupt structure ...

7 An interactive network of time-sharing computers

Ronald M. Rutledge, Albin L. Vareha, Lee C. Varian, Allan H. Weis, Salomon F. Seroussi, James W. Mayer, Joan F. Jaffe, Mary Anne K. Angell
August 1969 **Proceedings of the 1969 24th national conference**

Full text available: 📄 pdf(697.04 KB)   Additional Information: <u>full citation, abstract, references, citings, index terms</u>

This paper describes the design and implementation of an experimental interactive time-sharing network of computers created as a joint effort by Carnegie-Mellon University (CMU), Princeton University and the Research Division of IBM. The motivation behind the creation, the functional capabilities, and applications of the network are some of the key points addressed. Design philosophy and major implementation considerations are thoroughly explored. At present, all network nodes are IBM 360 M ...

8 Teaching operating systems in a virtual machine environment

John L. Donaldson
February 1987 **ACM SIGCSE Bulletin , Proceedings of the eighteenth SIGCSE technical**

symposium on Computer science education, Volume 19 Issue 1

Full text available: 📄 pdf(559.60 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

9 <u>The Brown University Student Operating System</u> ☐

David S. Wile, Robert G. Munck, Andries Van Dam
January 1967 **Proceedings of the 1967 22nd national conference**

Full text available: 📄 pdf(1.12 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The tenet that students taking a computer science course (even, and especially, an introductory one) should get actual machine experience has gradually been accepted in the past few years. The Brown University Student Operating System (SOS) provides: 1) such a "cut-down" assembler; 2) an interpreter for simulating the simplified machine whose code the assembler produces; 3) a control program which optimizes program storage, provides line-by-line program editing, and g ...

10 <u>General topics in computer science II - GCS II: Utilizing IBM plug-compatible disks on</u> ☐
<u>the CDC channel</u>

John W. Nall
March 1980 **Proceedings of the 18th annual Southeast regional conference**

Full text available: 📄 pdf(429.44 KB)   Additional Information: <u>full citation</u>

11 <u>Asynchronous interactions on shared data</u> ☐

William W. Collier
April 1973 **ACM SIGOPS Operating Systems Review,** Volume 7 Issue 2

Full text available: 📄 pdf(616.38 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>

In order to write algorithms which interact successfully on shared data, it is necessary to understand the assumptions which are made about each operation and to know whether or not the assumptions are met by the underlying hardware. There are several basic types of operations on shared data; from the number of these operations a measure of the degree of interaction between algorithms can be obtained. This measure can then be used to ensure that all instances of interaction are tested.

12 <u>Systems and techniques: A program simulator by partial interpretation</u> ☐

Kazuhiro Fuchi, Hozumi Tanaka, Yuriko Manago, Toshitsugu Yuba
October 1969 **Proceedings of the second symposium on Operating systems principles**

Full text available: 📄 pdf(637.75 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>

In promoting the ETSS project a program simulator based on an idea of partial interpretation has been constructed, and its principle and design are described in the paper. This new approach has been introduced to provide the simulator with such features as high speed and high accuracy in simulation and simplification in implementation. The essence of the idea of partial interpretation is using direct execution of instructions by hardware and simulation of them by an interpreter in combination, w ...

13 <u>Operating systems from assembler to C</u> ☐

John L. Donaldson
February 1990 **ACM SIGCSE Bulletin , Proceedings of the twenty-first SIGCSE technical symposium on Computer science education,** Volume 22 Issue 1

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1 A system for general-purpose analog-digital computation

Walter F. Bauer, George P. West
January 1956 **Proceedings of the 1956 11th ACM national meeting**

Full text available: 🔲 pdf(312.71 KB)   Additional Information: full citation, abstract, index terms

Concerning the age-old argument on the relative merits of analog and digital computation, it has been said that the true future for scientific computation involves the union of the equipments by means of an analog-digital converting device. In this period prior to the digital computer with fractional-microsecond arithmetic commands and memory accesses, it appears that for many problems such a union is necessary. Or, from another point of view, considerable machine time savings can be realiz ...

2 Wide channel computers

Stanley Lass
July 1987 **ACM SIGARCH Computer Architecture News**, Volume 15 Issue 3

Full text available: 🔲 pdf(240.09 KB)   Additional Information: full citation, abstract, citings, index terms

The wide channel design approach integrates vector and general purpose computing. Wide word accesses to a cache move several instructions per access or a few operands per access, thereby allowing a slower and somewhat larger cache. Channel commands control the movement of wide words in wide channels. Wide channels load data, vectors, instructions, and channel commands over wide channels to channel registers. Arithmetic unit operations are performed on the data in channel registers. Wide channels ...

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        Birke, D.;
        Communications, IEEE Transactions on [legacy, pre - 1988]
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